Ultra High Capacitance, Small Case Size Options



Type EDL electric double layer supercapacitors offer extremely high capacitance values (farads) in a variety of packaging options that will satisfy, low profile, surface mount, through hole and high density assembly requirements. The EDL is a cut above the standard electrolytic capacitor in that it can act as a battery without having to deal with the environmental or hazardous material issues that batteries entail.

Highlights

- Unlimited charging and discharging capability
- Recycling is not necessary
- Long Life 15 years
- Low ESR
- Will extend battery life up to 1,6 times
- First class performance with economy pricing

Specifications

Operating Temperature Range

Rated Voltage Range

Capacitance Range

Capacitor Tolerance

Back Up Time Example

2.1 Vdc to 5.5 Vdc 0.022 F to 70 F

-25 °C to +85 °C

-20/+80%

allow for product changes.

How To Select an Electric Double Layer Capacitor

Estimated Initial Backup Time

Select the optimum supercapacitor according to applied current

The internal resistance of the supercapacitor prevents drawing high discharge currents. Select the supercapacitor capable of delivering the peak current at switchover to back-up mode using the following table.

Back-up time for Type EDL Electric Double Layer Supercapacitors decreas-

es with use and over time especially when the current is large or operating at high temperature. Be sure to specify extra back-up time initially to

	Maximum Operating (Discharge) Current					
Series	0.047 F	0.1 F to 0.33 F	0.47 F to 1.5 F	3.3 F to 4.7 F	10 F to 50 F	
SG, SD, NF	200 µA	300 µA	1 mA	-	-	
F	200 µA	300 µA	300 µA	-	-	
EN	-	10 µA	-	-	-	
HW	-	_	-	300 mA	1 A	

Back-up time is the time it takes for the applied voltage to decay to the cut-off voltage set by the user after applying the application's maximum voltage at application maximum temperature.

Example: An F Type EDL, P/N EDLF105B5R5C (Rated at 5.5 V, 1.0 F) is charged to 5.0 Vdc. The circuit requirement is such that it must maintain a memory circuit with a current drain of 10 μ A in an ambient temperature of +40 °C. The memory RTC cut-off voltage is 2.0 Vdc.

Using minimum capacitance, calculate the back-up time as follows:

 $t = C\Delta V / I = C[V0-(i \cdot R)-V1] / (i+iL)$

C = 1.0 F–20% = 0.8 F, R=50 $\Omega,$ V0=5 V, V1=2 V, i=10 μA Therefore,

 $t = 0.8 (5-0.0005-2)/((10+2) \times 10^{-6}) = 55$ hours And thus the initial back-up time is 55 hours. After 1000 hours, calculate the back-up time will drop to about 38 hours.

t: Back-up time (s)

- C: Capacitance of Type EDL (F)
- V₀: Applied voltage (V)
- V₁: Cut-off voltage (V)
- i: Current during back-up (A)
- i_L: Leakage current (A)
- R: Internal resistance (Ω) at 1 kHz

Type EDL , Electric Double Layer Supercapacitors Ultra High Capacitance, Small Case Size Options

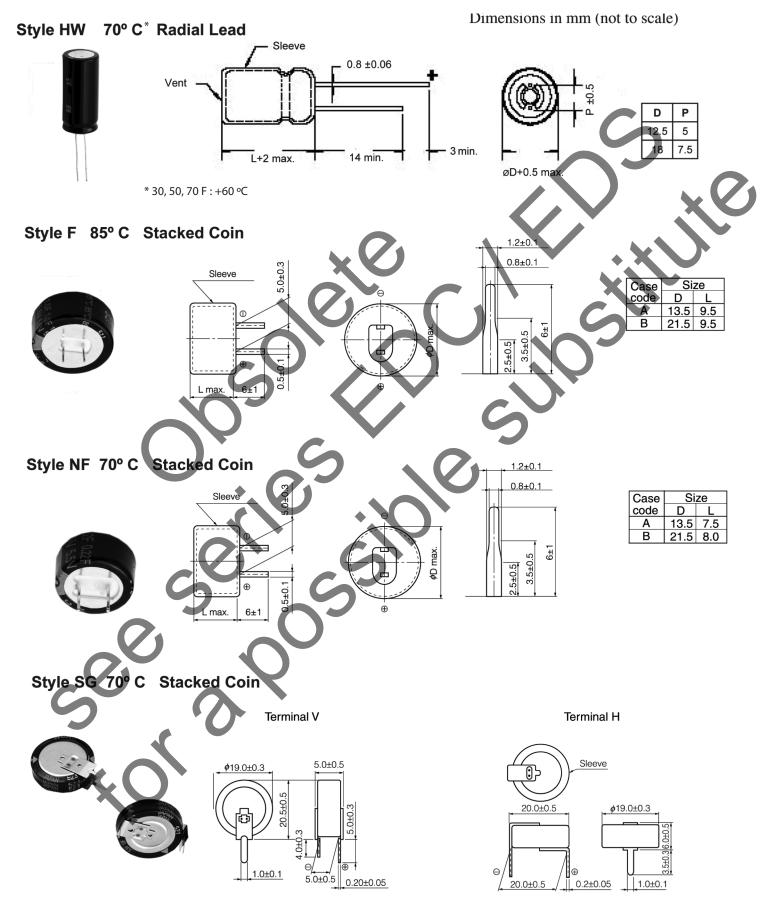
Life Design		i	Type EDL supercapacitors have a useful lifetime that decreases with ncreasing operating temperature, humidity, applied-voltage, current and backup-time requirements.
		E	Expected lifetime is the product of four factors:
			Expected Life = (Lifetime)•(Temperature Factor)•(Voltage Factor)•(Moisture Factor)
Lifetime		r	The minimum rated life at 85 °C with 5.5 Vdc applied is 1000 hours with maximum permitted end-of-life capacitance change of -30% and a 4 times increase in internal resistance.
Temperature Factor		t c v v v t	To determine the effect of temperature on expected life of a supercapaci- tor, use the fact that expected lifetime doubles for each 10 °C that the operating temperature is reduced. As an illustration, at 85 °C and full voltage the rated lifetime is 1000 hours. So, at 40 °C the expected lifetime would be multiplied by 2(85-40)/10 = 2^4.5 = 22.6 times. The Tempera- ture Factor is 22.6, and for 1000-h, 85 °C rated life, the expected 40 °C life would be 22600 hours.
Voltage Factor			The rate of change of capacitance decreases with decreasing applied voltage. The effect on life extension is roughly proportional to the voltage derating, e.g., 5 V applied to 5.5 V rated supercapacitors extends the life 1.1 times.
Moisture Factor			Expected life of these supercapacitors is considerably shortened by operation in high humidity. The applications discussed here assume that the relative humidity is no more than 50%.
Expected Life Example	00	t	50, for a 5.5 V supercapacitor at 40 °C charged to 5.V in less than 50% RH the expected life is: Expected Life = (Lifetime) (Temperature Factor) (Voltage Factor) (Moisture Factor) = (1000 h) (22.6) (1.1) (1) = 24800 hours = 2.8 years
	•	RoHS	Compliant
Curr	S	capacitor Cons	Activated Current eparator Carbon Collector
S			
0-	Rsn		
	Resistor to Charge	Electric Double Layer Capacitor	Resistor to Move lons

Catalog Part Number	Capacitance	Voltage (Vdc)	Max. Resistance @ 1 kHz (Ω)	Case Type	Case Dia. (mm)	Case Length (mm)	Style
EDLHW335D2R3R**	3.3 F		0.3	~	12.5	23	HW
EDLHW475D2R3R**	4.7 F	1	0.3		12.5	23	
EDLHW106D2R3R**	10 F		0.2		12.5	35	
EDLHW226D2R3R**	22 F	2.3	0.1	Radial Lead	18	35	³³ ± 0 23
EDLHW306D2R3R**	30 F	-	0.1	Leud	18	35	
EDLHW506D2R3R**	50 F		0.1		18	40	
EDLHW706D2R1R**	70 F	2.1	0.1		18	50	
EDLF473A5R5C^	0.047 F		120		13.5	9.5	
EDLF104A5R5C	0.10 F	-	100		13.5	9.5	
EDLF474B5R5C	0.47 F	5.5	75	Stacked	21.5	9.5	d Va.a
EDLF684B5R5C	0.68 F	-	50	Coin	21.5	9.5	
EDLF105B5R5C	1.00 F		50		21.5	9.5	+85 °C
EDLNF104A5R5C^	.10 F		75		13.5	7.5	NF
EDLNF224A5R5C	.22 F	\sim	75		13.5	7.5	5.0
EDLNF474B5R5C	.47 F	5.5	30	Stacked Coin	.21.5	8.0	15 D
EDLNF105B5R5C	1.0 F		30		21.5	8.0	T
EDLNF155B5R5C	1.5 F		30		21.5	8.0	+70 °C
EDLSG474V5R5C	.47 F		30	Stacked	19	5.0	SG
EDLSG105V5R5C	1.0 F	5.5	30	Coin	19	5.0	- +
EDLSG155V5R5C	1.5 F		30	Stacked Coin	19	5.0	
EDLSG474H5R5C	.47 F		30		20	6.0	
EDLSG105H5R5C	1.0 F	5.5	30		20	6.0	
EDLSG155H5R5C	1.5 F		30	\mathbf{v}	20	6.0	+70 °C
EDLSD223V5R5C**	.022 F		150	× · · · · · · · · · · · · · · · · · · ·	10.5	5.0	SD
EDLSD473V5R5C**	.047 F		120		10.5	5.0	0D
EDLSD104V5R5C^	.10 F	5.5	75	Stacked	10.5	5.0	20
EDLSD224V5R5C	.22 F		75	Coin	10.5	5.0	
EDLSD334V5R5C	.33 F		75		10.5	5.0	
EDLSD223H5R5C**	.022 F		150		11.5	5.5	
EDLSD473H5R5C**	.047 F		120		11.5	5.5	
EDLSD104H5R5CA	10 F	5.5	75	Stacked Coin	11.5	5.5	1 7
EDLSD224H5R5C	.22 F		75	COIN	11.5	5.5	
EDLSD334H5R5C	.33F	-	75		11.5	5.5	+70 °C
EDLEN204A3R3S**	.20 F	3.3	200	SMT Wide Lead	6.8	1.8	
X							
EDLEN204RL3R3S**	.20 F	3.3	200	SMT Radial Lead	6.8	1.8	

** Product is obsolete and no longer available.

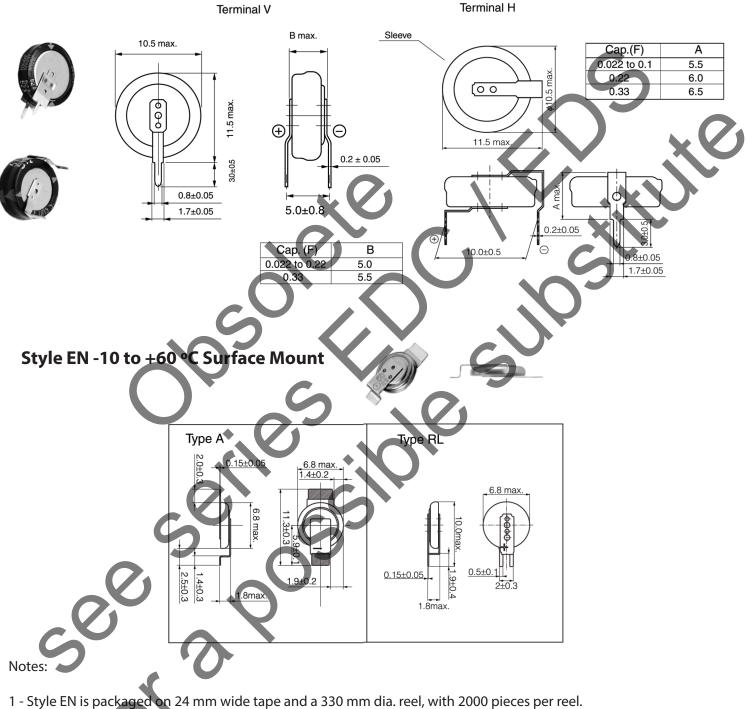
^ Product has been discontinued, replacement part is part-number below discontinued number.

Outline Drawing and Dimensions



Outline Drawing and Dimensions

Style SD 70 °C Stacked Coin



2 - Only Style EN is capable of reflow soldering. Peak reflow soldering temperature is 250 °C for a maximum of 5 seconds, with a maximum of 30 seconds at or above 220 °C.

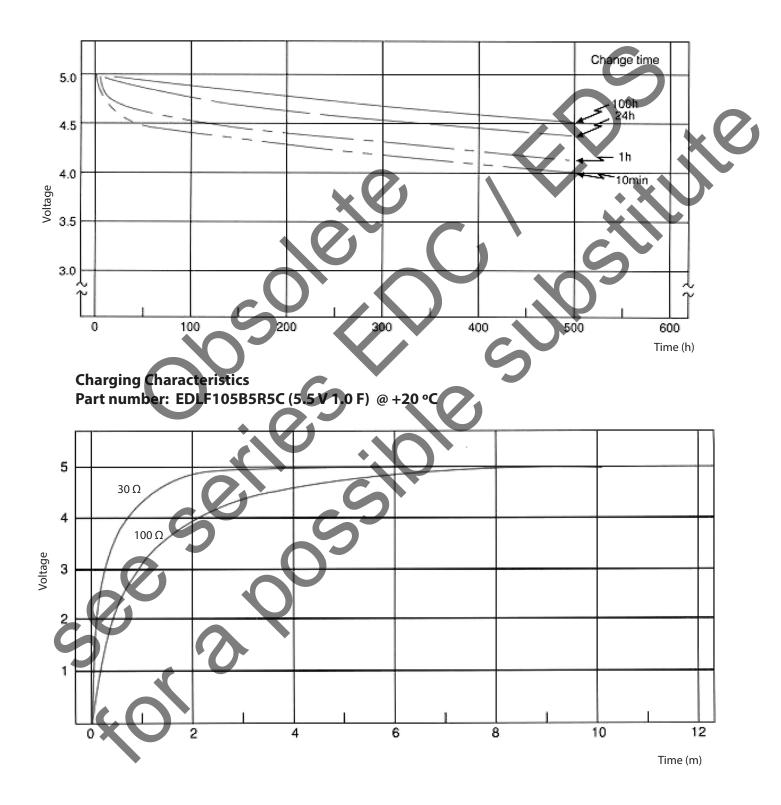
3 - Do not reflow solder when the cell voltage is above 0.3 V.

Applications and Recommended Series

Application	Function	Recommended Series	Component		
Mobile Phones Real-Time Clock Back-Up					
PDA	Real-Time Clock Back-Up	– EN	6		
DSC	Real-Time Clock Back-Up	EN, SD			
DVD Recorder	Real-Time Clock and Channel Back-Up	SD, SG			
Digitial TV	Real-Time Clock and Channel Back-Up	SD, SG NF			
PC, Server	Real-Time Clock and Channel Back-Up	F			
Mobile Phone Base Station	Real-Time Clock and Channel Back-Up	6			
Inkjet Printer	Time and Impact Back-Up	SD, SG, NF			
Electric Power	Real-Time Clock and Data Back-Up.	F	1) III III		
Gas and Water Meters					
LED Light	LED Lighting at Night				
with Solar Battery Toys	Motor Drives	– HW			
ToyGames	Real-Time Clock Back-Up	EN	R.		
Robot	Real-Time Clock and Data Back-Up	- F	.0e 10		
Car Audio Memory	Car Audio Memory Real-Time Clock Back-Up				

Performance Data

Self-Discharging Characteristics Versus Charging Time Part number: EDLF105B5R5C (5.5 V 1.0 F) Charge voltage: 5V



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