## FEATURES
High Pulse Currents - High voltage

## APPLICATIONS
Power Semiconductor Circuits – SCR Commutation
Ballast controls – Switching Power Supplies

<table>
<thead>
<tr>
<th>Operating Temperature Range</th>
<th>-55°C to +105°C</th>
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<tbody>
<tr>
<td>Capacitance Tolerance</td>
<td>±10% at 1 kHz, 25°C +5% optional</td>
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<table>
<thead>
<tr>
<th>AC voltage (50/60 Hz)</th>
<th>WVDC 250</th>
<th>400</th>
<th>630</th>
<th>1000</th>
<th>1600</th>
<th>2000</th>
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<tbody>
<tr>
<td>VAC</td>
<td>160</td>
<td>200</td>
<td>400</td>
<td>630</td>
<td>650</td>
<td>700</td>
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For T>+85°C, The voltage (DC/AC) must be decreased by (1.5/2.25)% per °C

<table>
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<tr>
<th>Dissipation Factor (MAX)</th>
<th>25°C</th>
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<tr>
<td>Frequency (kHz)</td>
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<tr>
<td>C≤0.1uF</td>
<td></td>
</tr>
<tr>
<td>0.1uF&lt;C≤1uF</td>
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<tr>
<td>C&gt;1uF</td>
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<tr>
<td>1</td>
<td>0.05%</td>
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<tr>
<td>10</td>
<td>0.05%</td>
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<tr>
<td>100</td>
<td>0.16%</td>
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| Insulation Resistance @25°C (<70% RH) for 1 minute at 100VDC applied |
|--------------------------|-----------------------------|
| Capacitance              | Insulation Resistance       |
| <0.33μF                  | 100000 MΩ                   |
| >0.33μF                  | 30000 MΩ×μF                |

| Self Inductance | <1 nano-Henry per mm of lead spacing |
| Load Life       | 2000 Hours, +85°C with 125% of rated voltage |
| Capacitance Change | <1% of initially measured value |
| Dissipation Factor | <0.001 at 10kHz and 25°C for C≤1uF |
| Insulation Resistance | <0.001 at 1kHz and 25°C for C>1uF |
| Reliability (0.5xRated Voltage, 40°C) | >50% of maximum specified value |
| 1 FIT=1 failure/1 billion component hours |
| Capacitance Change | <10% of initially measured value |
| Dissipation Factor | <200% of initially specified value |
| Insulation Resistance | >50% of maximum specified value |

| Damp Heat test | 56 days at 40°C with 90 to 95%RH, +40°C and no voltage applied |
| Capacitance Change | <5% of initially measured value |
| Dissipation Factor | <0.005 at 1kHz and 25°C |
| Insulation Resistance | >50% of maximum specified value |

| Self Inductance | <1 nano-Henry per mm of lead spacing |
| Capacitance Drift Factor | <0.5% after 2 years at 40°C |
| Capacitance Temperature Coefficient | -200 ppm/°C, ±100ppm/°C |

<table>
<thead>
<tr>
<th>Dielectric Strength Terminal to Terminal</th>
<th>Terminal to case</th>
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<tr>
<td>160% of rated VDC or 150% VAC applied for 2 Seconds and 25°C</td>
<td>3kVAC @ 50/60 Hz applied between terminals and case for 60 seconds at 25°C</td>
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<table>
<thead>
<tr>
<th>Dielectric Construction Plastic Case and Epoxy Resin Leads</th>
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<tbody>
<tr>
<td>Polypropylene Metallized film Flame Retardant materials (UL 94V-0) Lead free tinned copper leads</td>
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</tbody>
</table>

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Aug-19
Permissible (sinusoidal) AC voltage versus frequency for a temperature rise of 10°C
Not for across the line applications
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<tr>
<th>WVDC</th>
<th>Capacitance (µF)</th>
<th>IC PART NUMBER</th>
<th>dv/dt (v/µsec)</th>
<th>Dims LxHxT (mm)</th>
<th>S (MM)</th>
<th>d (MM)</th>
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<td>560</td>
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